

# Medium Power Transistor (32V, 1A)

## MP6X1

### ●Applications

Low frequency amplifier

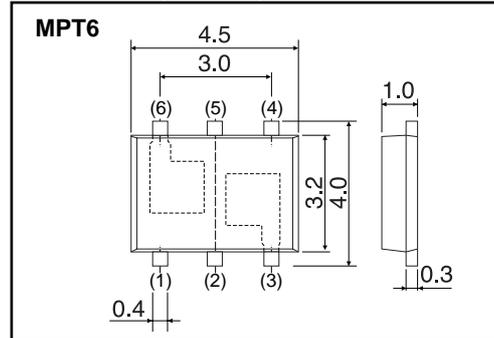
### ●Features

- 1) Low  $V_{CE(sat)}$ ,  $V_{CE(sat)} = 0.15V$ (Typ.)  
( $I_C/I_B = 500mA/50mA$ )
- 2) Contain two 2SD1664-dies in a package.

### ●Structure

NPN silicon epitaxial planar transistor

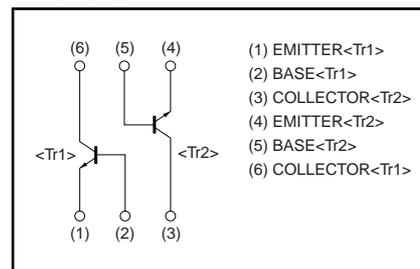
### ●Dimensions (Unit : mm)



### ●Packaging specifications

Type	Package	Taping
	Code	TR
	Basic ordering unit (pieces)	1000
MP6X1		○

### ●Inner circuit



### ●Absolute maximum ratings ( $T_a=25^\circ C$ )

<Tr1, Tr2>

Parameter	Symbol	Limits	Unit	
Collector-base voltage	$V_{CBO}$	40	V	
Collector-emitter voltage	$V_{CEO}$	32	V	
Emitter-base voltage	$V_{EBO}$	5	V	
Collector current	Continuous	$I_C$	1	A
	Pulsed	$I_{CP}^{*1}$	2	A
Power dissipation	$P_D^{*2}$	2.0	W / TOTAL	
		1.4	W / ELEMENT	
Junction temperature	$T_j$	150	$^\circ C$	
Range of storage temperature	$T_{stg}$	-55 to +150	$^\circ C$	

\*1  $P_w=10ms$  1Pulse

\*2 Mounted on a ceramic board

Transistors

●Electrical characteristics (Ta=25°C)

<Tr1, Tr2>

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Collector-emitter breakdown voltage	BV <sub>CEO</sub>	32	–	–	V	I <sub>c</sub> =1mA
Collector-base breakdown voltage	BV <sub>CB0</sub>	40	–	–	V	I <sub>c</sub> =50μA
Emitter-base breakdown voltage	BV <sub>EBO</sub>	5	–	–	V	I <sub>E</sub> =50μA
Collector cutoff current	I <sub>cBO</sub>	–	–	500	nA	V <sub>CB</sub> =20V
Emitter cutoff current	I <sub>EBO</sub>	–	–	500	nA	V <sub>EB</sub> =4V
Collector-emitter saturation voltage	V <sub>CE(sat)</sub> *	–	–	400	mV	I <sub>c</sub> /I <sub>B</sub> =500mA/50mA
DC current gain	h <sub>FE</sub>	120	–	390	–	V <sub>CE</sub> =3V, I <sub>c</sub> =100mA
Transition frequency	f <sub>t</sub> *	–	150	–	MHz	V <sub>CE</sub> =5V, I <sub>E</sub> =–50mA, f=100MHz
Collector output capacitance	C <sub>ob</sub>	–	15	–	pF	V <sub>CB</sub> =10V, I <sub>E</sub> =0A, f=1MHz

\* Pulsed

●Electrical characteristics curves

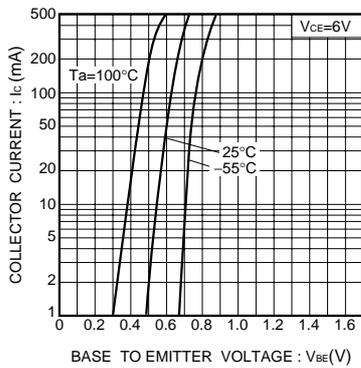


Fig.1 Grounded emitter propagation characteristics

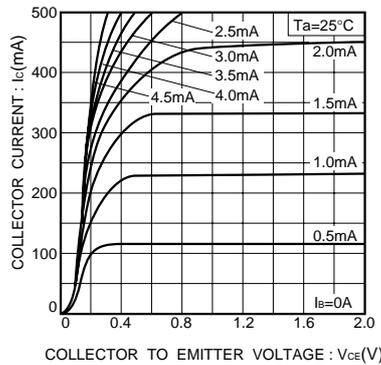


Fig.2 Grounded emitter output characteristics

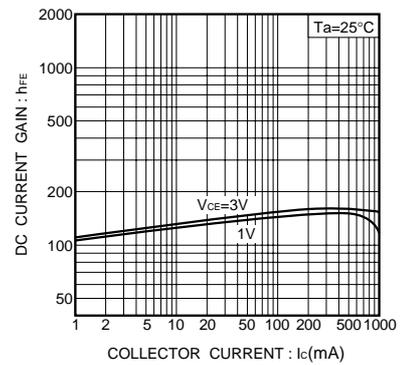


Fig.3 DC current gain vs. collector current ( I )

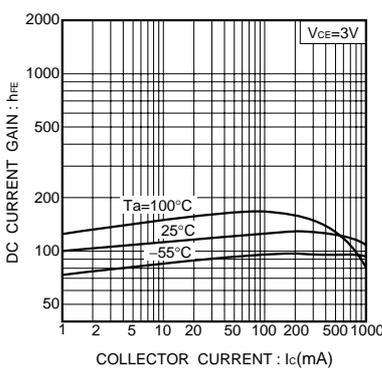


Fig.4 DC current gain vs. collector current (II)

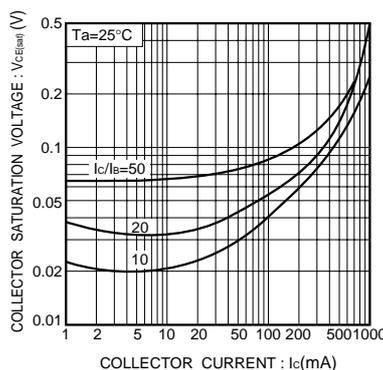


Fig.5 Collector-emitter saturation voltage vs. collector current ( I )

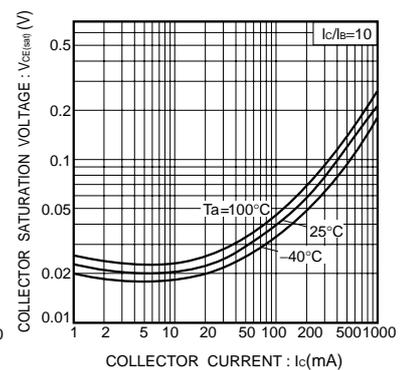


Fig.6 Collector-emitter saturation voltage vs. collector current (II)

Transistors

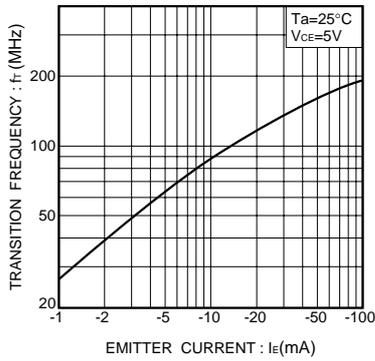


Fig.7 Gain bandwidth product vs. emitter current

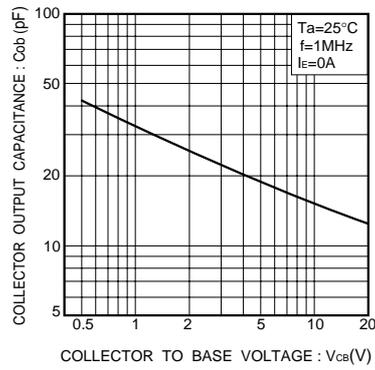


Fig.8 Collector output capacitance vs. collector-base voltage

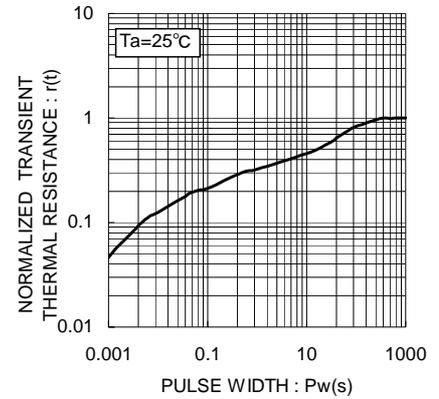


Fig.9 Normalized thermal resistance (Element)

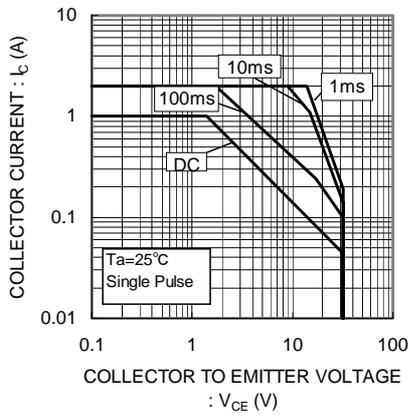


Fig.10 Safe operating area (Tr1&Tr2)

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